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Call for Papers

The IEEE International Symposium on Design and Diagnostics of Electronic Circuits and Systems provides a forum for exchanging ideas, discussing research results, and presenting practical applications in the areas of design, test, and diagnosis of electronic circuits and systems. The DDECS Symposium series has been organized by these European countries: Czech Republic (1997, 2002, 2006, 2009, 2013), Poland (1998, 2003, 2007, 2014), Slovakia (2000, 2004, 2008), Hungary (2001, 2005), Austria (2010), Germany (2011), Estonia (2012), and Serbia (2015).

Topics of interest include but are not limited to:

<i>SoC and NoC Design and Test</i>	<i>Design and Test in Nano-Technologies</i>
<i>ASIC/FPGA Design</i>	<i>Analog, Mixed-Signal, RF Design and Test</i>
<i>Built-in Self-Test and Self-Repair</i>	<i>ATE Hardware and Software</i>
<i>Bio-Inspired Hardware</i>	<i>Design for Testability and Diagnosis</i>
<i>Design Verification/Validation</i>	<i>On-line Testing</i>
<i>Formal Methods in System Design</i>	<i>Embedded Systems</i>
<i>Hardware/Software Co-Design</i>	<i>Memory, Processor Testing</i>
<i>IP-based Design</i>	<i>MEMS Testing</i>
<i>Logic Synthesis</i>	<i>Physical Design</i>
<i>Defect/Fault Tolerance and Reliability</i>	<i>Dependable HW/SW Systems</i>

Submissions

Prospective authors are cordially invited to submit original papers using the electronic submission system at the Symposium web page. Papers in English with a length of 6 pages maximum in IEEE conference style are expected. Specialized student and industrial sessions, as well as embedded tutorials, will be organized at the symposium. Accepted papers will be included in the Symposium Proceedings and submitted for inclusion into IEEE Xplore as well as other Abstracting and Indexing databases (WoS, Scopus, etc). An extra work in progress session will be targeted to get early feedback on in-progress research and preliminary results.

Important Dates

Submission deadline:	10th January 2016
Notification of acceptance:	15th March 2016
Camera-ready papers:	21th March 2016

Symposium Location

Košice, the metropolis of eastern Slovakia, is situated on the river Hornád near the borders with Hungary, Poland and Ukraine. Being the economic and cultural centre it is the seat of the Košice region parliament, the Slovak Constitutional Court, three universities, various dioceses, and many museums, galleries, and theatres. Košice has a well-preserved historical centre, which is the largest among Slovak towns. In 2013 it was the European Capital of Culture.

Further Information

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