novelIC MICROSYSTEMS

*Novel***IC** is a CHIP (Fabless) Company providing High-Tech Design Solutions.

*Novel***IC** people have developed a reputation of high quality designs on challenging projects. The core engineering team boasts demanding projects in the fields of Digital IC Design (FPGA and ASIC), communications, signal processing, RF/mm-Wave, Analog and Analog/Mixed-Signal IC design. NovelIC has dynamic and creative working environment with excellent conditions. We are offering:

Internships
In the area of **Analog IC Design**

We are starting with the various programs for the students in the final years of B.Sc. and M.Sc. studies. You will be trained and supervised by our experienced engineers, working on the exciting projects, cutting-edge of today's Analog/RF/mmWave IC design.

The following programs are possible, depending on the student's current status and common agreement.

- Full-time internships in our facilities; 40h weekly; scholarship provided; duration 6 months.
- Part-time internships in our facilities; 20h weekly; scholarship provided; duration 2-3 months.

Depending on candidate knowledge and preferences, various topics in area will be covered – from the design of operational amplifiers and comparators, design of PMICs - Power Management ICs (bandgap reference, current references, LDOs, switching DC/DC power converters) to analog signal processing blocks approaching AD converters (VGAs, filters, ADC drivers, calibration circuits) as well as design of systems like ADCs/DACs. All these circuits are important parts of most modern communication and sensor systems.

After successful completion of the internship the intern can use the results for writing their B.Sc. or M.Sc. theses in the agreement with the professor from the University and our side. Moreover, satisfying results of the full-time internship provide a good chance of offering a full-time position within the NovelIC Design Centre in Belgrade to the graduated intern.

Your profile:
- Final year or Master degree student of Electronics
- A hard-working student with good marks (GPA above 8.5).
- A positive, motivated person and team player.
- Strong theoretical background in analog, linear electronics and IC design topics.
- The following skills are advantageous: proficiency with circuit simulators, EDA tools such as Virtuoso.
- Solid knowledge of English. Intermediate level at least.

Please send us your CV and brief motivation letter by the email to the following address: careers@novelic.com.

Your email should have the following subject: **NIC_Internship_Analog**

Your CV should be in PDF format.

Deadline for applications: **15. May 2016.** The interviews with the selected candidates will be held in **May 2016**, after which the final selection is to be made. The starting point of the program is to be individually defined for each selected intern.

For more insight check out our webpage www.novelic.com.

We are looking forward to meeting you!