



**Novellic** is a B2B Company providing High-Tech Design Services and Solutions in SoC/FPGA, Embedded and Analogue/RFIC electronics.

**Novellic** develops novel 60 GHz Radar Sensor technologies.

**Novellic** people have developed a reputation of high quality designs on challenging projects.

Novellic has dynamic and creative working environment with excellent working conditions. Responsibility and opportunity to lead and drive the projects is given. Stable long-term career roadmap for proven team members is our goal, so as yours. We invest in excellence, offer excellence and ask for excellence.

We are proud to offer a position to strengthen our Digital IC and Embedded Group.

## **DSP and FPGA/ASIC Engineer**

### **1 full-time position**

Critical aspects of the role in general include architecture and implementation of building blocks inside of complex digital IC systems. We encourage ideas, ability to understand algorithms and blocks to the deepest level and focus on implementation. We encourage people ready to think wide and deep on the architecture level. At least one finished SoC project implemented on FPGA or ASIC platform is deemed as necessary.

You should be experienced in some of the following:

- RTL level modelling and implementation in VHDL and/or Verilog.
- C/C++. SystemC is an advantage.
- FPGA/ASIC/DSP implementation.
- Matlab and Simulink. Signal processing.
- Knowledge of embedded HW/SW is considered advantageous.

Common skills:

- Education level: Dipl.-Ing or Master Degree. PhD encouraged.
- Experience in high-scale projects with reporting, presentations, conferences, subversion systems, bug tracking etc.
- Proficiency in English, level B2 at least.

We offer:

- Working in a good team with good engineers.
- Challenging R&D and industrial world-class projects.
- Attractive salary packet: salary, bonus, private rent scheme, highly paid business trips.
- Taking part in ownership of the company through equity options.
- Support of PhD enrolling.

If you are interested please send us your CV (in PDF format) and brief motivation letter by the email to the following address: [info@novellic.com](mailto:info@novellic.com).

Your email should have the following subject: <NIC\_Job\_201511\_YourName >

Deadline for applications: **23.Dec 2015**. The interviews with the selected candidates will be held **in Dec 2015**, after which the final selection is to be made. For more insight check out our webpage [www.novellic.com](http://www.novellic.com).

We are looking forward to meeting you! Novellic Team.