HDL Design House is one of the best companies for ASIC/System-on-Chip Design/Verification and Physical Implementation. We offer a challenging work environment, competitive salaries, additional benefits as well as opportunities for skills acquisition and development. Currently we are looking for motivated and creative individuals to join our team. Engineers with no prior experience are also desirable, making this ideal opportunity for a recent graduate engineer to fast-track their career development into a more senior role.

Position: **Analog Design Engineer – multiple positions in Belgrade and Ćuprija**
Location: Belgrade and Ćuprija, Serbia

The ideal candidate will have the following skills and background:

- **M.Sc.EE or equivalent degree in Electronics, Computer Science or Electrical engineering**
- **Highly motivated and dynamic engineer able to adopt new technology quickly**
- **Knowledge of mixed-signal design and/or analog control system design**
- **Understanding of advanced CMOS process nodes and SoC design flows to drive analog design and integration inside SoC**
- **Experience with tools for schematic entry, physical layout, and design verification**
- **Proficiency in running SPICE and Mixed-Signal simulations**

The following skills will be a plus:

- **Analog design experience**
- **Experience with state of the art analog design tools**
- **Experience with the implementation technologies below 90nm/CMOS and all steps in analog design flow**
- **Experience with RF design**
- **Supervision of layout and hands-on layout experience in advanced CMOS technologies**
- **Knowledge in Verilog-A/AMS for analog behavioral modeling**

Your key responsibilities:

- **You will be part of the team working on the implementation of advanced analog IPs**
- **Simulations and circuit design will be part of your daily tasks**
- **Design, implement, and simulate the functionality and performance of other various low power high resolution analog circuits;**
- **Craft layout floor plans to optimize the overall performance; Supervise the layout activities and give concise guidelines to layout engineers, need to be hands on in drawing layout if necessary;**
- **Develop the analog testing plans and work with the PE/TE teams to characterize the functionality and performance of the products to ensure the quality;**
- **Need to comply with the team's design methodologies and release flows.**

We offer:

- **Comprehensive training for Juniors from experts for design/analog/verification of ASIC/FPGA System-on-Chip**
- **Unique chance to join a rapidly expanding company offering fantastic career and skills development opportunities**
- **An exceptional salary**
- **Permanent employment**
- **Private health insurance for all family members**
- **Opportunity to develop soft skills through series of training**
- **Opportunity to be involved in many sports activities with colleagues**
- **Opportunity to travel abroad and work on our clients' cites**
- **Working in young and enthusiastic team**

Proficient knowledge of UNIX/Linux and fluent English is a prerequisite for all candidates!

Please send your CV to jobs@hdl-dh.com